# Product Document

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Application Note: AS5048B I2C Slave Address Programming

# AS5048B

# 14-bit Rotary Position Sensor with Digital Interface



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### **Revision History**

Revision	Date	Owner	Description
1.0	29.04.2013	ACH	Initial revision
1.1	18.11.2013	RPH	Added note for 3.3V programming; Added Information about burn status and gload operation; Minor changes



### **1. General Description**

This Application Note explains how to programm the slave address for the AS5048B 14-bit Rotary Position Sensor.

If more than two AS5048B are used or if there is already a device on the I2C bus with the same adress, it might be necessary to change the slave adress of the AS5048B.

To permanently program configuration on the AS5048, an OTP block is used. First the desired values are written into the desired register. A special command described below enables the automatic programming of the OTP. After programming, the programmed bits are verified.

### Note:

The programming can be performed in 5V operation mode. If the AS5048 is used in 3V operation mode, a minimum supply voltage of 3.3V and an additional capacitor with a value of  $10\mu$ F is necessary on the VDD3V pin to perform the programming procedure.

### Note:

The programming has to be performed at room temperature.

#### Note:

The status of the burning procedure can be seen by polling the Burn bit in the OTP control register. Refer to Figure 3.

#### Note:

After programming, it is recommended to verify the OTP block content. To load the content from the OTP block into the registers using modified comperator levels, the Verify bit in the OTP control register is used.

### 2. Programming the AS5048B slave address

The following programming sequence demonstrates how the AS5048B slave address can be changed.

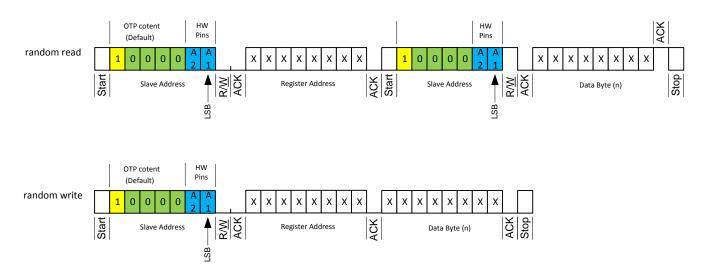
The slave address consists of the hardware setting on pins A1, A2 and upper MSBs programmable by the user. The MSB of the slave address (yellow) is internally inverted. This

### AS5048B I2C Slave Address Programming



means that by default the resulting data is '1'. A read of the I<sup>2</sup>C slave address register 21 will return a '0' at the MSB.

# Figure 1: Slave address construction



### Set a new slave address:

Write new slave address into I<sup>2</sup>C slave address register (dec.21)

### Program the OTP bits permanently:

- 1. Write dec.253 into the OTP control register (dec.3) to enable the special programming mode
- 2. Set the Burn bit (dec.8) in the OTP control register (dec.3) to enable automatic programming procedure
- 3. Write dec.0 into the OTP control register (dec.3) to disable the special programming mode



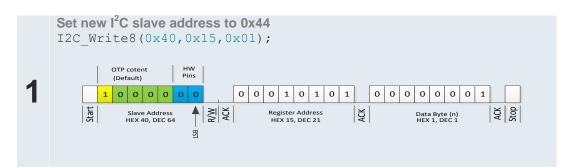
### Note:

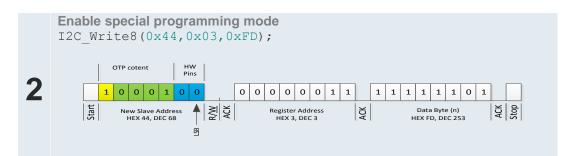
Use the special programming mode only to program the slave address!

### C-Code EXAMPLE:

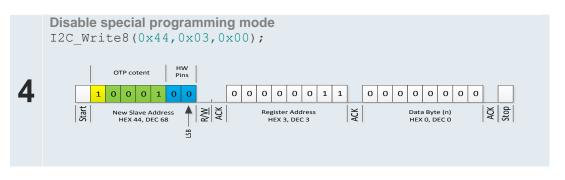
Figure 2:

4 Steps to program permanently a new I<sup>2</sup>C slave address











# 3. AS5048B I<sup>2</sup>C Register Map

The available registers for  $I^2C$  communication of the AS5048A are listed in Figure 3.

Figure 3: I<sup>2</sup>C Register Map

	Address Access						
	dec	Name	Туре	Bit Nr.	Symbol	Default	Description
				7	not used Verify		
٩				5	veniy		Programming control register.
Control OTP	3	3 Programming Control	R/W	4	not used	0	Programming must be enabled before burning the
itro				3	Burn		fuse(s). After programming is an verification mandatory
Cor				2	reserved		See programming procedure.
-				1			
				0	Programming Enable		
				7		n.a.	
s				:	not used		I <sup>2</sup> C slave address
ting		I <sup>2</sup> C slave	R/W + Program	5			slave address consist of 5 programable bits (MSBs) and the hardware setting of Pins A1 and A2 I <sup>2</sup> C address <4> is by default not programmed and due
Programmable Customer settings	21	21 address		4	I <sup>2</sup> C address<4>	internally inverted	
ner				:	:		to the inversion defined as '1'
stor				0	I <sup>2</sup> C address<0>	0	
Cn		OTP Register	R/W	7	Zero Position <13>	0	
le	22	Zero Position	+	:	:	:	Zero Position value high byte
nab		Hi	Program	0	Zero Position <6>	0	
Ĩ			Ŭ	7			
gra		OTP Register	R/W	6	not used	n.a.	
Pro	23	23 Zero Position	+	5	Zero Position <5>	0	Zero Position remaining 6 lower LSB's
		Low 6 L	Low 6 LSBs	Program	:	:	:
				0	Zero Position <0>	0	
	250	Automatic	<b>_</b>	7	AGC value<7>	1	Automatic Gain Control value.
		Gain	R	0	AGC value<0>	0	0 decimal represents high magnetic field 255 decimal represents low magnetic field
		Control		7		0	200 declinal represents low magnetic field
	251	: not used n.a.   251 Diagnostics R 3 Comp High 0   2 Comp Low 0 0 Diagnostics   1 COF 0 0   0 OCF 1 1   7 Magnitude<13> 0			not used	n.a.	
			R				Diagnostic flags
					Comp Low		
						-	
Readout Registers	252		4				
gist	202		R	0	Magnitude<6>	0	4
Re			R	7	Wayintuue<0>	0	
out		Magnitude		6	not used	n.a.	Magnitude information afer ATAN calculation
ade	253	253		5	Magnitude<5>	0	
Re	200			:	:	:	
				0	Magnitude<0>	0	
	254	254 Angle	R	7	Angle<13>	0	
				:	i i	:	1
				0	Angle<6>	0	Angle Value afer ATAN calculation and zero position adder
	255		R	7 6	not used	n.a.	
				5	Angle -Es	0	
					Angle<5>		
				: 0	Angle<0>	: 0	4
				U	Angle<0>	U	



# 4. Ordering Information

Table 1: Ordering Information

Ordering Code	Description	comments
AS5048B-HTSP	14 –Bit Programmable Magnetic Rotary	I <sup>2</sup> C interface
	Encoder with I2C-Interface	



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